



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

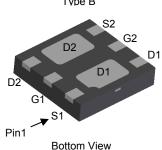
Device	V _{(BR)DSS}	R _{DS(ON) max}	I _{D MAX} T _A = +25°C
		$29m\Omega$ @ $V_{GS} = 4.5V$	5.6A
Q1 N-Channel	12V	$34m\Omega$ @ $V_{GS} = 2.5V$	5.1A
		$44m\Omega$ @ $V_{GS} = 1.8V$	4.5A
		$65m\Omega$ @ V_{GS} = 1.5 V	3.7A
	-12V	$61m\Omega$ @ $V_{GS} = -4.5V$	-3.8A
Q2 P-Channel		$81m\Omega @ V_{GS} = -2.5V$	-3.3A
		115mΩ @ V _{GS} = -1.8V	-2.8A
		170mΩ @ V _{GS} = -1.5V	-2.3A

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Power Management Functions

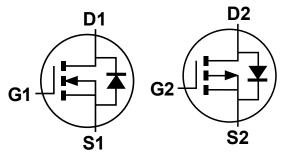


Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 64
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (approximate)



N-CHANNEL MOSFET P-CHANNEL MOSFET
Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMC1229UFDB -7	U-DFN2020-6 Type B	3000/Tape & Reel
DMC1229UFDB -13	U-DFN2020-6 Type B	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



D2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	(D		Е		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Q1 N-CHANNEL	Q2 P-CHANNEL	Units		
Drain-Source Voltage			V _{DSS}	12	-12	V
Gate-Source Voltage	V_{GSS}	±8	±8	V		
Continuous Brain Current (Note EVV - 4 EV	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	5.6 4.4	-3.8 -3.0	Α
Continuous Drain Current (Note 5) V _{GS} = 4.5V	t < 5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	7.2 5.8	-5.0 -4.0	Α
Maximum Continuous Body Diode Forward Curre	I _S	1	-1	Α		
Pulsed Drain Current (10μs pulse, duty cycle = 1	I _{DM}	20	-15	Α		

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	ם	1.4	W	
Total Power Dissipation (Note 5)	t < 5s	P_{D}	2.2	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	נ	92		
Thermal Resistance, Junction to Ambient (Note 5)	t < 5s	$R_{ hetaJA}$	55	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta JC}$	30			
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to 150	°C	

Note: 5. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.



Electrical Characteristics Q1 N-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage	BV_{DSS}	12	l	-	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	l	l	1.0	μΑ	$V_{DS} = 12V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	l	l	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	V _{GS(th)}	0.4	_	1	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		
			17	29		$V_{GS} = 4.5V, I_D = 5A$		
Static Drain-Source On-Resistance	_	-	20	34	mΩ	$V_{GS} = 2.5V$, $I_D = 4.6A$		
Static Dialif-Source Off-Resistance	R _{DS} (ON)	1	24	44		$V_{GS} = 1.8V$, $I_D = 4.1A$		
		l	30	65		$V_{GS} = 1.5V, I_D = 2A$		
Forward Transfer Admittance	Y _{fs}	l	6.5	-	S	$V_{DS} = 10V, I_{D} = 5A$		
Diode Forward Voltage	V_{SD}	_	0.6	1.2	V	$V_{GS} = 0V, I_{S} = 1A$		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	C _{iss}	1	914		pF	., ., ., .,		
Output Capacitance	Coss	l	132	1	pF	$V_{DS} = 6V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	C_{rss}	1	119		pF	1 - 1.01011 12		
Gate Resistance	R_g		1.26	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$		
Total Gate Charge (V _{GS} = 4.5V)	0	_	10.5	_	nC			
Total Gate Charge (V _{GS} = 8V)	Qg	_	19.6	_	nC	\/ - C\/ - C 5A		
Gate-Source Charge	Q_{gs}	_	1.2	_	nC	$V_{DS} = 6V, I_D = 6.5A$		
Gate-Drain Charge	Q_{gd}	_	1.6	_	nC			
Turn-On Delay Time	t _{D(on)}	_	5.0	_	ns			
Turn-On Rise Time	t _r	_	10.5	_	ns	$V_{DD} = 6V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t _{D(off)}		16.6	_	ns	$R_L = 1.2\Omega$, $R_G = 1\Omega$		
Turn-Off Fall Time	Ì _f		4.1	_	ns	1		

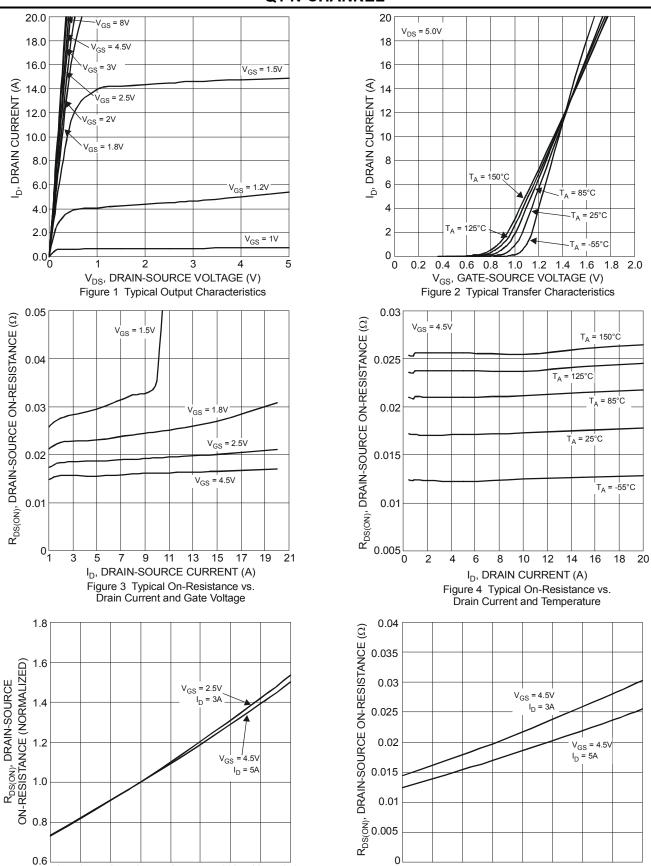
Electrical Characteristics Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage	BV _{DSS}	-12	1	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}			-1.0	μΑ	$V_{DS} = -12V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	V _{GS(th)}	-0.4		-1	V	$V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$		
		l	37	61		$V_{GS} = -4.5V$, $I_D = -3.6A$		
Static Drain-Source On-Resistance	Б		47	81	mΩ	$V_{GS} = -2.5V$, $I_D = -3.2A$		
Static Drain-Source On-Resistance	R _{DS (ON)}	_	63	115	11122	$V_{GS} = -1.8V, I_D = -1A$		
		_	90	170		$V_{GS} = -1.5V$, $I_D = -1A$		
Forward Transfer Admittance	Y _{fs}	_	5.5	_	S	$V_{DS} = -10V$, $I_{D} = -3.6A$		
Diode Forward Voltage	V_{SD}		-0.65	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	C _{iss}	l	915		pF	., ., ., .,		
Output Capacitance	Coss		225		pF	$V_{DS} = -6V, V_{GS} = 0V,$ - f = 1.0MHz		
Reverse Transfer Capacitance	C _{rss}	_	183	_	pF	1 - 1.0WI IZ		
Gate Resistance	R_g		56.9		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$		
Total Gate Charge (V _{GS} = -4.5V)	0	_	10.7	_	nC			
Total Gate Charge (V _{GS} = -8V)	Qg	_	17.9	_	nC	., ., ., ., .,		
Gate-Source Charge	Q _{qs}		1.7	_	nC	$V_{DS} = -6V, I_D = -4.3A$		
Gate-Drain Charge	Q _{qd}		3.0	_	nC			
Turn-On Delay Time	$t_{D(on)}$		5.7	_	ns			
Turn-On Rise Time	t _r		11.5	_	ns	$V_{DD} = -6V, V_{GS} = -4.5V,$		
Turn-Off Delay Time	t _{D(off)}		27.8	_	ns	$R_L = 1.6\Omega$, $R_G = 1\Omega$		
Turn-Off Fall Time	t _f	_	26.4	_	ns]		

6. Short duration pulse test used to minimize self-heating effect. 7. Guaranteed by design. Not subject to product testing. Notes:



Q1 N-CHANNEL



-50

25

50

T_J, JUNCTION TEMPERATURE (°C)

Figure 6 On-Resistance Variation with Temperature

75

100

100

125

25

50

T_{.I}, JUNCTION TEMPERATURE (°C)

Figure 5 On-Resistance Variation with Temperature

75

125



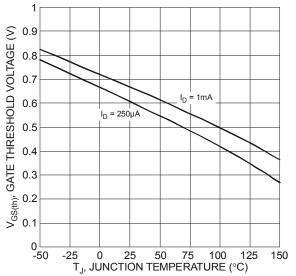
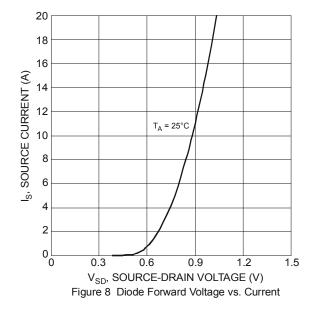
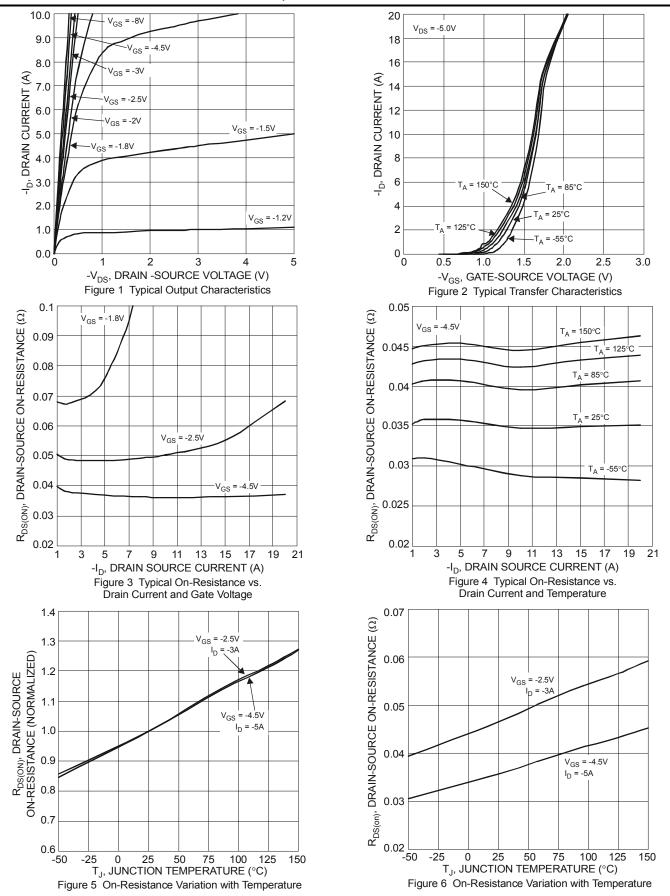


Figure 7 Gate Threshold Variation vs. Ambient Temperature





Q2 P-CHANNEL





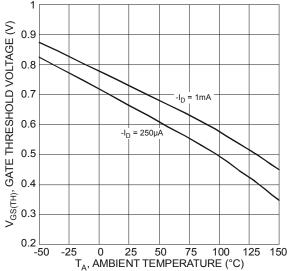
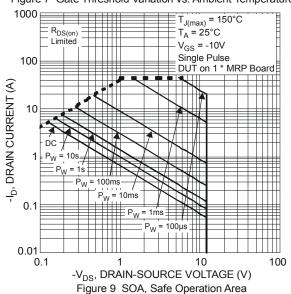


Figure 7 Gate Threshold Variation vs. Ambient Temperature



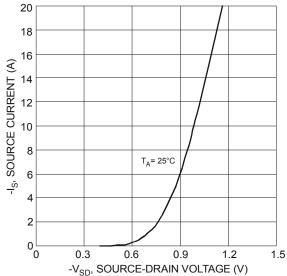
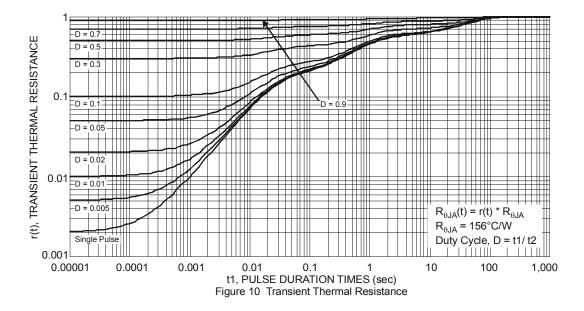


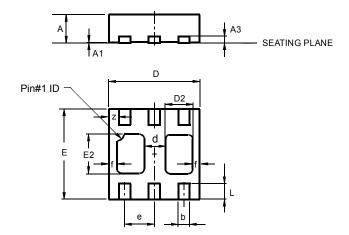
Figure 8 Diode Forward Voltage vs. Current





Package Outline Dimensions

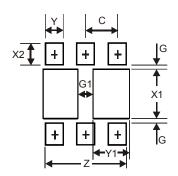
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-DFN2020-6								
Туре В								
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0	0.05	0.02					
A3			0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
d		_	0.45					
D2	0.50	0.70	0.60					
е	_	_	0.65					
E	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
f	_	_	0.15					
L	0.25	0.35	0.30					
z	_	_	0.225					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Υ	0.37
Y1	0.70
С	0.65



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